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Apply these concepts to perform computer arithmetic

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Express, simplify, and minimize Boolean functions, using truth tables, Boolean algebra, Karnaugh maps, and logic minimization software. Utilize technology mapping techniques, such as NAND only. Use decoders and multiplexers to implement a combinational logic circuit.

Represent sequential systems using finite state machines. Produce state diagrams, state tables, excitation tables and state equations, including one-hot state assignments.

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Use timing diagrams to explain basic sequential circuit timing issues

Analyze and design synchronous sequential



Analyze and design circuits using Datapath components: registers, adders, shifters,



Analyze and design multi-

Explain arithmetic logic units (ALUs), registers

Explain elementary register transfer level designs

Describe the organization and operation of a basic digital computer



Describe the difference between HDL and software

Develop simulation

Verify designs using a testbench

Use behavioral models of logic circuits

Map HDL models to logic circuits on field programmable gate arrays (FPGAs)



Explain the hierarchical relationship between transistors, digital logic, and microprocessors

Explain the operation of machine language vis a vis computer architecture

Analyze and revise basic assembly language computer programs using a 5-11 b



Implement and debug combinational and sequential logic circuits using standard



Implement and debug combinational and sequential logic circuits using HDL and FPGAs

Implement and debug basic systems using



Design, simulate, build, validate, debug, and document digital circuits



- x Number systems
- x
- x
- x Datapath Components
- x Hardware Description Language (HDL)
- x Introduction to Assembly Language
- x
- x Design process

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